B .E /B .Tech DE GRE E E XA MIN A TION ,MA Y /JU N E $2\,012$. SIXTH SE ME STE R

E LE CTRICA L A N D E LE CTRON ICS E N GIN E E RIN G MICROPROCE SSOR A N D MICROCON TROLLE RS (RE GU LA TION 2008)

ANSWER ALL QUESTIONS	
PART A-(1 0*2=20)	

- 1 .If the cry stal oscillator frequency is 5M $\,$ H $\,$ Z ,calcrlate the tim e period to execute the instruction STA4200 in 8085 processor..
- 2.what is the function of PCH L instruction?
- 3. State the function of DSR and DTR pins in 8251?
- 4. what are the different operating m odes of 8253 tim er?
- 5.what phy sical address is represented by 437 0H :567 1 H and 9A65H :0599H ?
- 6.Illustrate the operation of the following instructions in 8086 processor.
- (a) DAA.
- (B) CBW.
- 7 .list rhe addressing m odes of 8051.
- 8. what is the function of PORT 3 In8051 m icrocontroller?
- 9.what is the resolution of 1 0 bit ASDC?
- 1 0.m ention any two applications of stepper m otor control.

PART A-(5*1 6=80 m arks)

- 1 1.(a)(i) nam e and explain the different addressing m odes used in 8085 with suotable exam ples.(8)
- (ii) write an 8085 assem bly language program to evaluate the expression w=x2+y2+z2.(8) (OR)
- (B)(i) Draw the tim ing diagram for the instruction IN82h of 8085 processor.(8)
- (ii)explain the hardware interrupts of 8085 processor.(8)
- 1 2.(a) with neat functonal block diagram, explain the features of 8255 program m able peripheral interface and its program m ing m odes.(1 6)
- (b) design a circuit to interface 8251 USART with 8085 processor to handle serial data transfer poerations.(1 6)
- 1 3.(a)(i) what are the flages available in 8086 processor? Explain their functions.(8)
- (ii)explain the instructions supporting string operations in 8086 m icroprocessor.

write an ALP to m ove a block of data starting from m em ory location X to m em ory location starting from Y.(8)

(or)

(b)(i)draw the pin diagram of 8086 CPU and explain the functions of the pins in m inim um and

m axim um mode of operation of 8086. (10)
(ii)describe the circuit suitable for generation of multiple wait states for 8086 CPU module.(6)

1.4. (a)explain the architecture of 8051 microcontroller with neat block diagram. (16)
(or)
(b)(i)draw the circuit to interface 8K RAM with 8051 externally.explain the circuit.(8)
(ii)explain the timer operation in 8051 microcontroller with necessary diagram s.(8)

1.5. (a)An industrial monitoring system requires monitoring nine different parameters and actuating three different tasks, three parameters per task use tnterrupt structy ure provide suitable input and output devices for accessing the system. design an 8051 microcontroller based system for this application and explain. (16)
Or
(b)draw the circuit to interface a DC motor to 8051 microcontroller.write an 8051 assembly language program to control the speed of the motor.explain the circuit and algorithm. (16)